Alumni Activities-2020-2021

Dept. of E.C.E

1. Online guest lecture on "**How to prepare for GATE**" was conducted on 13th August,2020 for III B.Tech. ECE students.

   Resource Persons:

Mr. P. Naveen & P. Prannoy
PVPSIT Alumni
(ECE 2015-19 Batch)
M.TECH @ IIT Bombay

2. Online guest lecture on "**Architecture & Logic Design**" was conducted on 21st August, 2020 for III B.Tech. ECE students.

   Resource Person:

Sinjini Madduri,
Logic Design Engineer at Intel Corporation, Massachusetts.
PVPSIT Alumni, (ECE 2007-11 Batch)
She has been working for Server Development Group for the past five years. She has been involved in discussions of high-level architecture, planning and implementation of several features for Server IPs. Before this, she worked at Broadcom Corporation as a Post Silicon Characterization Intern. She has a Masters degree from University of Massachusetts, Amherst in Computer Engineering.
Sinjini strongly believes in the impact of women in the field of engineering and plays an active role in Women at Intel Network.

3. An interactive session on "**Opportunities in Core, Software, and Govt. sector**" was organized on 9th January, 2021 for I B.Tech. ECE Students.

   Resource Persons:

   Mr.V.Sasi Bhushan, Pega lead architect., CISCO (B.Tech.-Alumni 2006-10)

   Mr. Sarath, Project Manager (Smart cities Dept.), Effectronics (B.Tech.-Alumni 2012-15)

   Ms. Jeevanti and Ms. Nasareen , Assistant System engineer, TCS (B.Tech.-Alumni 2014-18)

   Mr. P. Naveen, M.Tech., IIT Bombay (B.Tech.-Alumni 2015-19)